IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRELIMINARY AMENDMENT ACCOMPANYING APPLICATION

APPLICANT:	Hideo Yamanaka	ATTY. DOCKET NO. 09792909-5696
SERIAL NO.		GROUP ART UNIT:
DATE FILED:	October 7, 2003	EXAMINER:
INVENTION:	"METHOD AND APPARATUS FOR PRODUCING ULTRA-THIN SEMICONDUCTOR CHIP AND METHOD AND APPARATUS FOR PRODUCING ULTRA-THIN BACK-ILLUMINATED SOLID-STATE IMAGE PICKUP DEVICE"	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

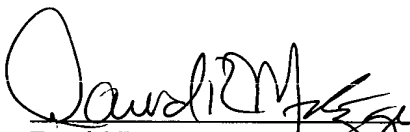
S I R:

Between the title and the heading "Background of the Invention" on page 1, insert the following:

--RELATED APPLICATION DATA

The present application claims priority to Japanese Application(s) No(s). P2002-299563 filed October 11, 2002, which application(s) is/are incorporated herein by reference to the extent permitted by law.--

Respectfully submitted,



(Reg. No. 32,919)
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